

Please replace the paragraph beginning at page 1, line 5 with the following new paragraph:

B2 This application claims priority to the copending provisional patent application, Serial Number 60/243,708, Attorney Docket Number CYPR-CD00167, entitled "Advanced Programmable Microcontroller Device," with filing date October 26, 2000, and assigned to the assignee of the present application.

Please replace the paragraph beginning at page 4, line 18 with the following new paragraph:

B3 In the present embodiment, the analog blocks are arranged in an array on a single integrated circuit, or chip. The analog system architecture can be generally referred to as a programmable analog "system-on-a-chip" block. Such programmable blocks can be used in those applications that typically require multiple chips that may be fabricated using different technologies. Implementation in embedded applications, including audio, wireless, handheld, data communications, Internet control, and industrial and consumer systems, is contemplated.

Please replace the paragraph beginning at page 12, line 17 with the following new paragraph:

B4 A PROGRAMMABLE ANALOG SYSTEM ARCHITECTURE

Please replace the paragraph beginning at page 33, line 1 with the following new paragraph:

B5 PROGRAMMING ARCHITECTURE FOR A PROGRAMMABLE ANALOG SYSTEM

Please replace the paragraph beginning at page 43, line 17 with the following new paragraph:

B6 The preferred embodiment of the present invention, programming architecture for a programmable analog system, is thus described. While the present invention has been described in particular embodiments, it should be appreciated that the present invention should not be construed as limited by such embodiments, but rather construed according to the following claims.

B7 Please replace the paragraph beginning at page 50, line 1 with the following new paragraph:

PROGRAMMING ARCHITECTURE FOR A PROGRAMMABLE ANALOG SYSTEM

VERSION OF AMENDMENTS WITH CHANGES SHOWN:

IN THE TITLE

Please replace the original title with the following new title:

PROGRAMMING ARCHITECTURE FOR [AN ANALOG] A PROGRAMMABLE ANALOG  
SYSTEM [ON A CHIP]

IN THE SPECIFICATION

Please replace the paragraph beginning at page 1, line 1 with the following:

PROGRAMMING ARCHITECTURE FOR [AN ANALOG] A PROGRAMMABLE ANALOG  
SYSTEM [ON A CHIP]

Please replace the paragraph beginning at page 1, line 6 with the following:

This application claims priority to the copending provisional patent application, Serial Number [60/243,798] 60/243,708, Attorney Docket Number CYPR-CD00167, entitled "Advanced Programmable Microcontroller Device," with filing date October 26, 2000, and assigned to the assignee of the present application.

Please replace the paragraph beginning at page 4, line 18 with the following new paragraph:

In the present embodiment, the analog blocks are arranged in an array on a single integrated circuit, or chip. The analog system architecture can be generally referred to as [an] a programmable analog "[programmable] system-on-a-chip[.]" [or PSoC,] block. [PSoC] Such programmable blocks can be used in those applications that typically require multiple chips that may be fabricated using different technologies. Implementation in embedded applications, including audio, wireless, handheld, data communications, Internet control, and industrial and consumer systems, is contemplated.

Please replace the paragraph beginning at page 12, line 17 with the following new paragraph:

[AN ANALOG] A PROGRAMMABLE ANALOG SYSTEM [ON A CHIP] ARCHITECTURE

Please replace the paragraph beginning at page 33, line 1 with the following new paragraph:

PROGRAMMING ARCHITECTURE FOR [AN ANALOG] A PROGRAMMABLE ANALOG SYSTEM [ON A CHIP]

Please replace the paragraph beginning at page 43, line 17 with the following new paragraph:

The preferred embodiment of the present invention, programming architecture for [an] a programmable analog [programmable] system [on a chip], is thus described. While the present invention has been described in particular embodiments, it should be appreciated that the present invention should not be construed as limited by such embodiments, but rather construed according to the following claims.

Please replace the paragraph beginning at page 50, line 1 with the following new paragraph:

PROGRAMMING ARCHITECTURE FOR [AN ANALOG] A PROGRAMMABLE ANALOG  
SYSTEM [ON A CHIP]